

r Reference: 2024.30

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lowrey, et al

Application Number: 10/072,369 Filing Date: February 8, 2002

Art Unit/Examiner: 2814/Hoai V. Pham

Title: PROGRAMMABLE RESISTANCE MEMORY ELEMENT AND METHOD

FOR MAKING SAME

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97(b)(3)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants submit this statement in accordance with their duty of disclosure under 37 CFR 1.56. This statement is filed in accordance with 37 CFR 1.97(b)(3). The Commissioner is hereby authorized to charge any fees to Deposit Account Number 051 1068. Applicant hereby notifies the U.S. Patent and Trademark Office of the items 152 ted on Forms PTO/SB/08A, submitted herewith.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicants do not waive any right to take any action that would be appropriate to antedate or otherwise remove the listed documents as a competent reference against the claims of the present application.

Respectfully submitted,

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June 6, 2003

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